A 23 21 TTL Direct Photometering Circuit TTL Photometering Circuit 16 S  $\infty$ 13 Diaphragm Control Circuit Motor Control Circuit Built-in Flash Circuit Shutter/ Control Circuit AF Circuit 9 13b 13a Pt Pu Ph X 百 Ē RAM ROM S P13 P12 P11 P10 В Ъ P 4 Ó S φ Communication Display Panel Communication Information Camera-lens Camera-flash EEPROM Interface Interface Information တ  $\mathfrak{C}$ Vdd DC/DCon 0 × Q × 0 Regulator **2a** Fig.

22

Pm

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Setting Switches

 $\sigma$ 

SWS SWR SWM

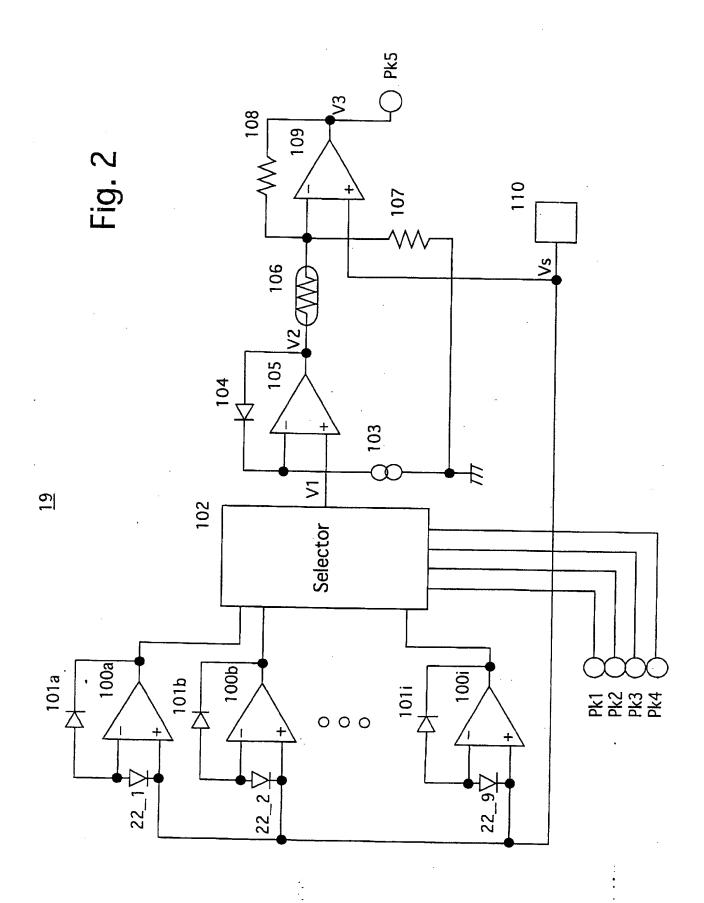


Fig. 3

<u>20</u>

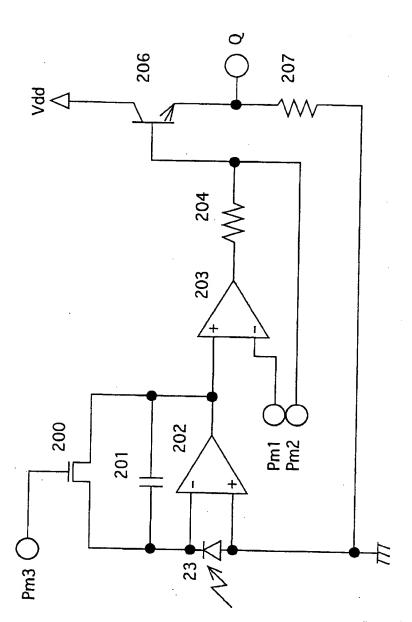


Fig. 4A

20

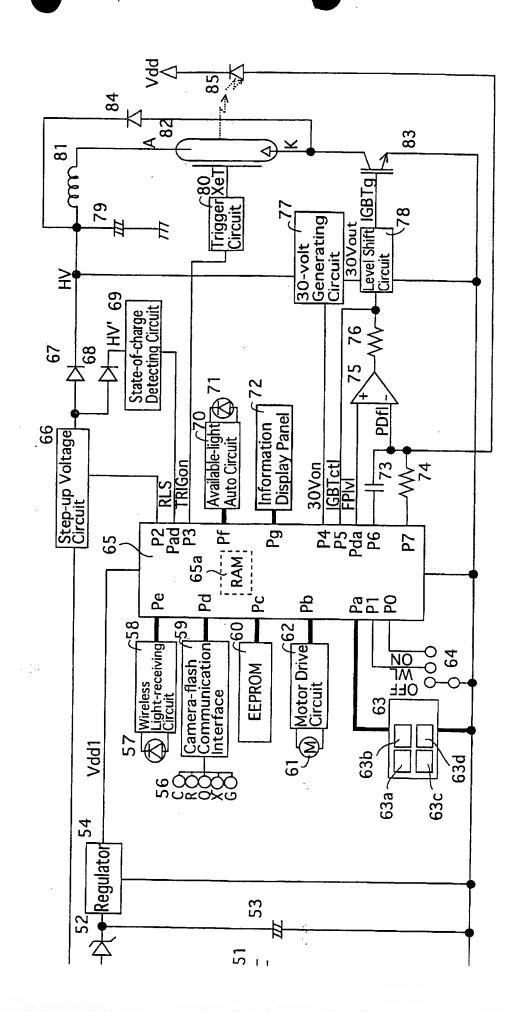


Fig. 4B

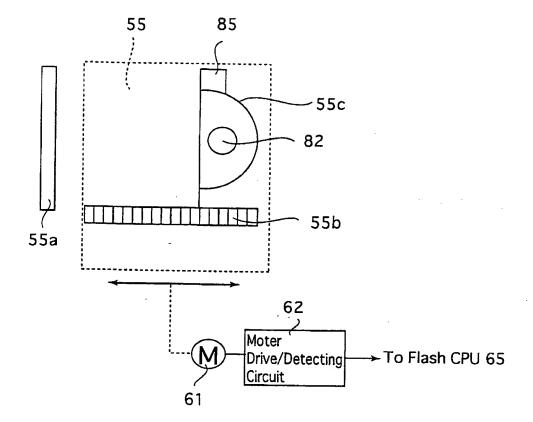


Fig. 5

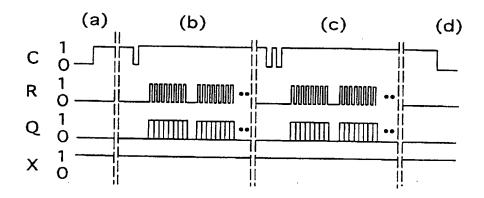
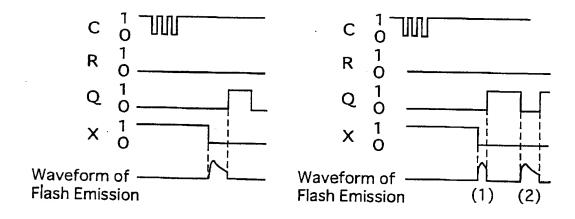
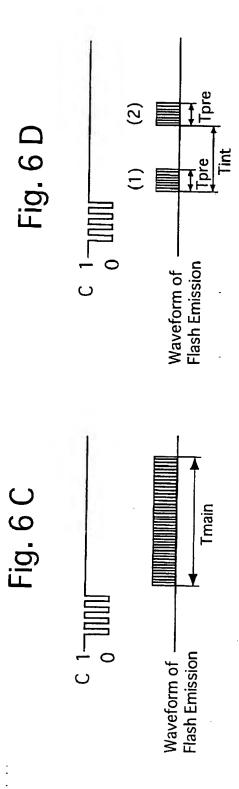


Fig. 6 A

Fig. 6 B





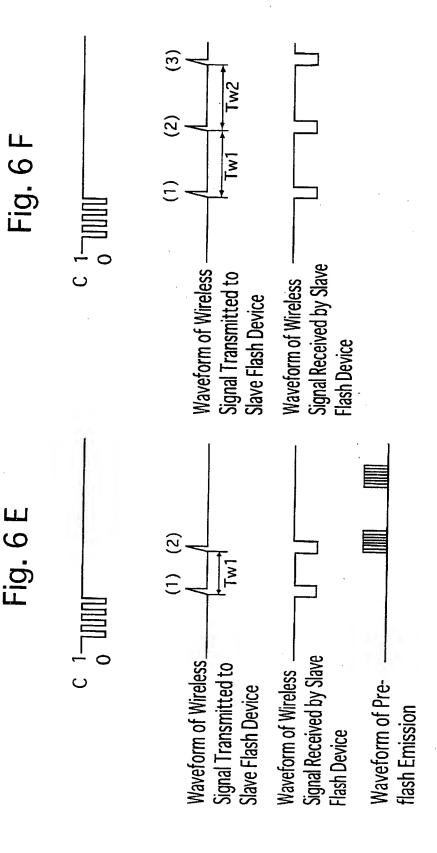


Fig. 7

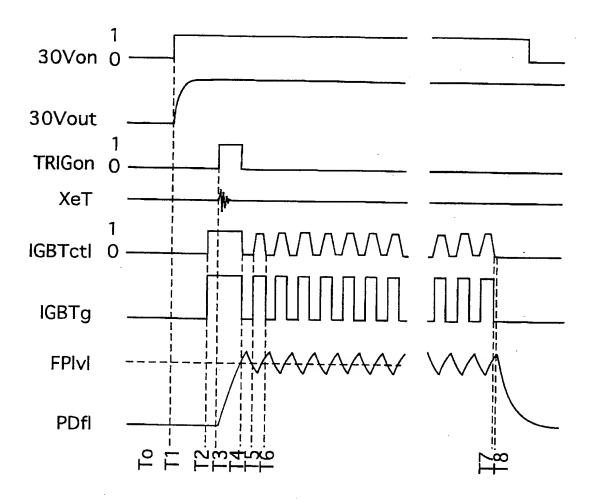


Fig.8A

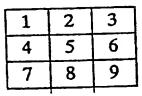


Fig.8C

4	12	4
12	36	12
4	12	4

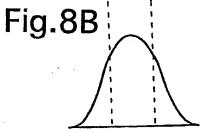


Fig.9A



Fig.9B

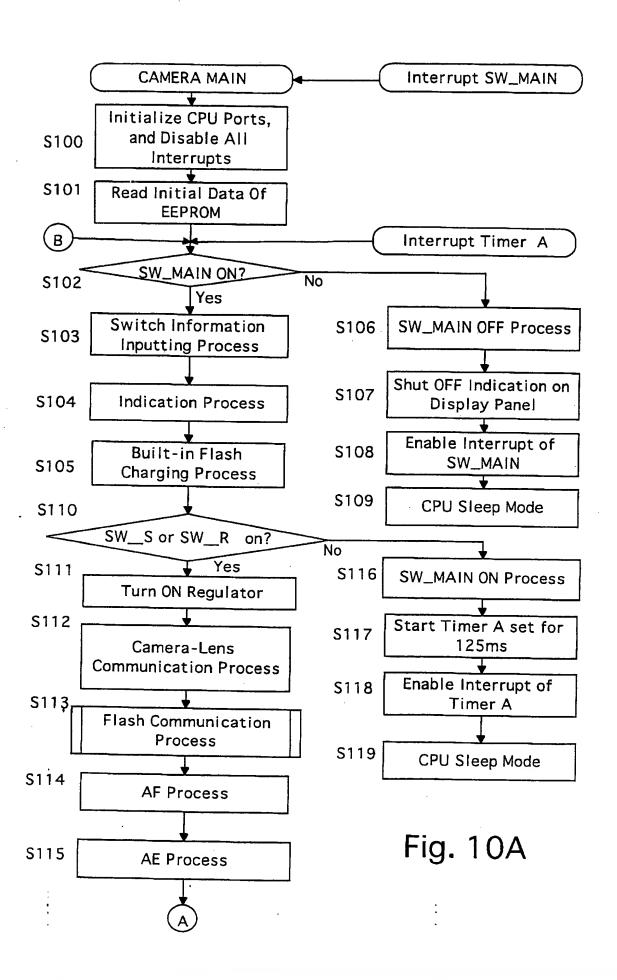
1	1	1
1	4	1
1	3	1

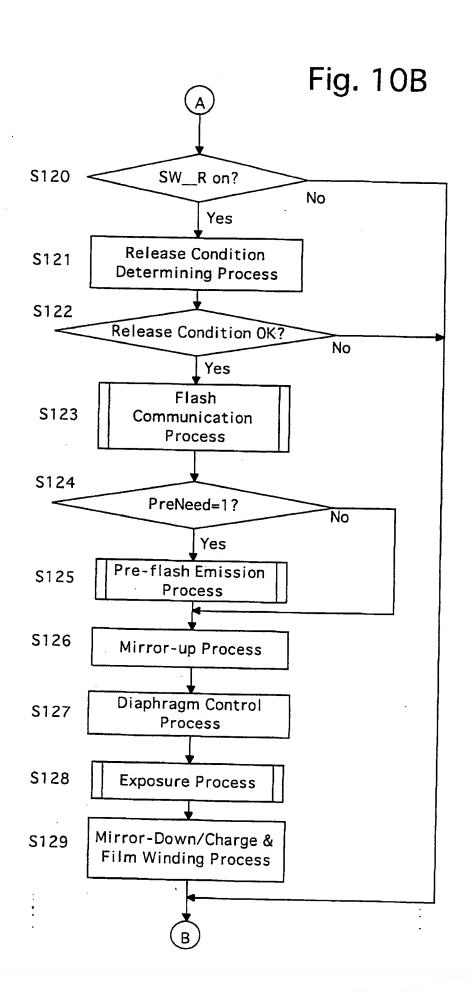
Fig.9C

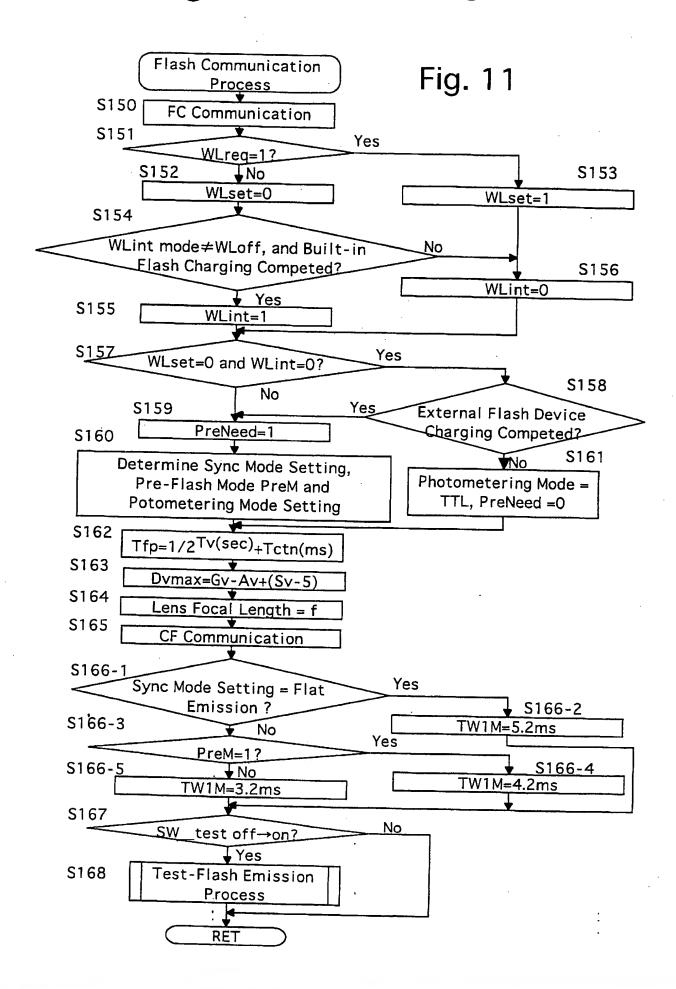


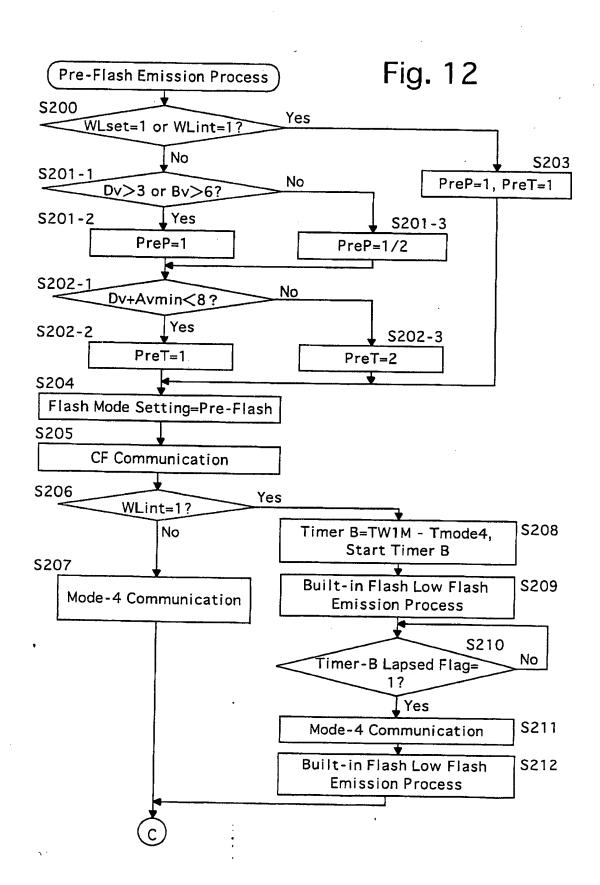
Fig.9D

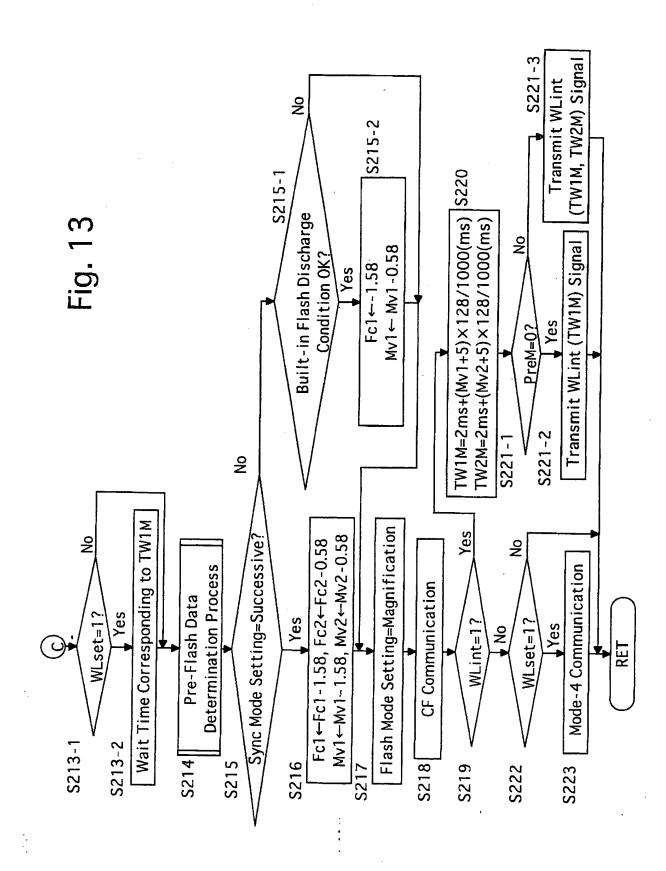
4	4	7
4	4	7
4	4	7

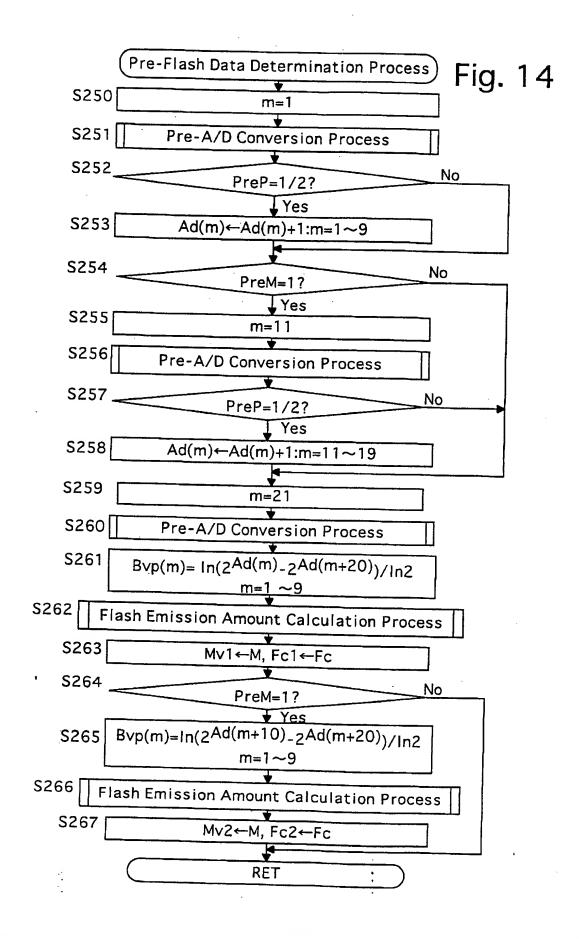


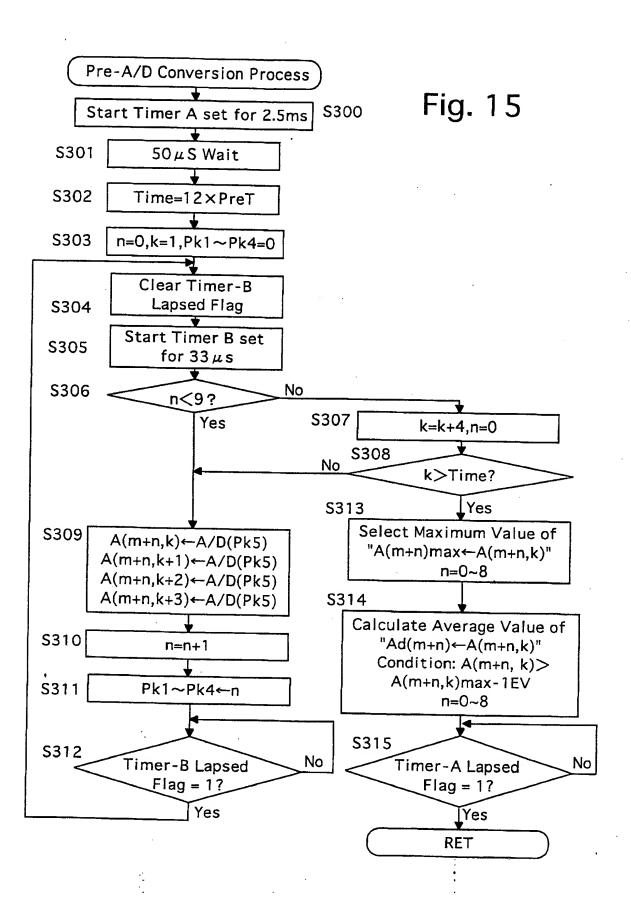


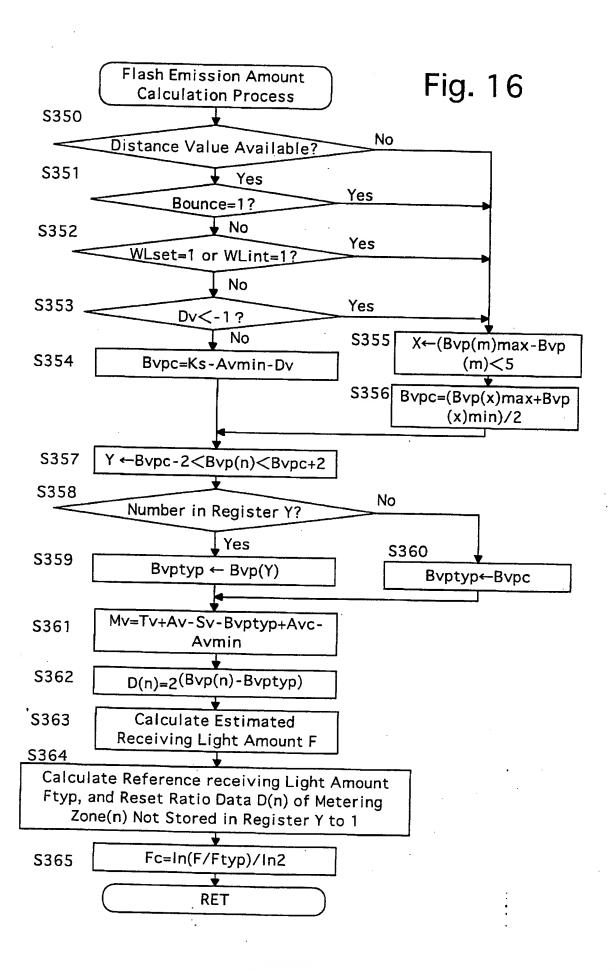


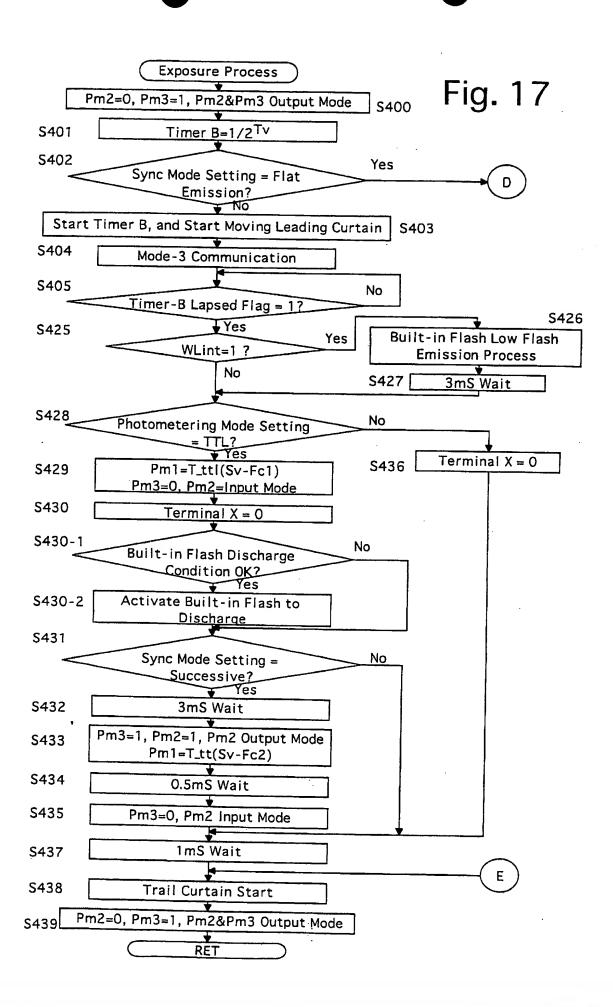


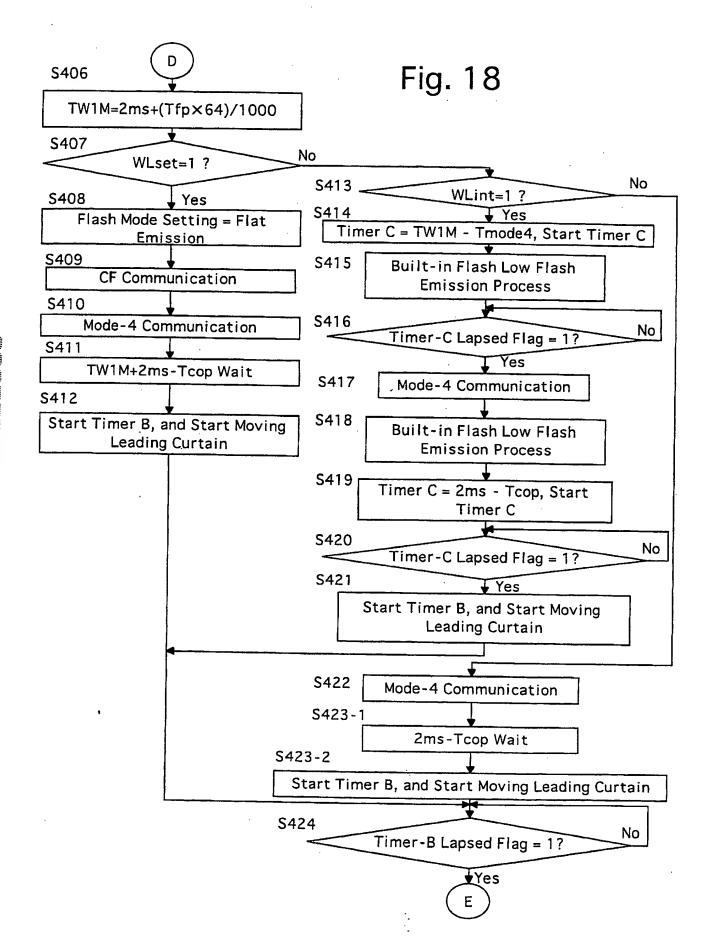


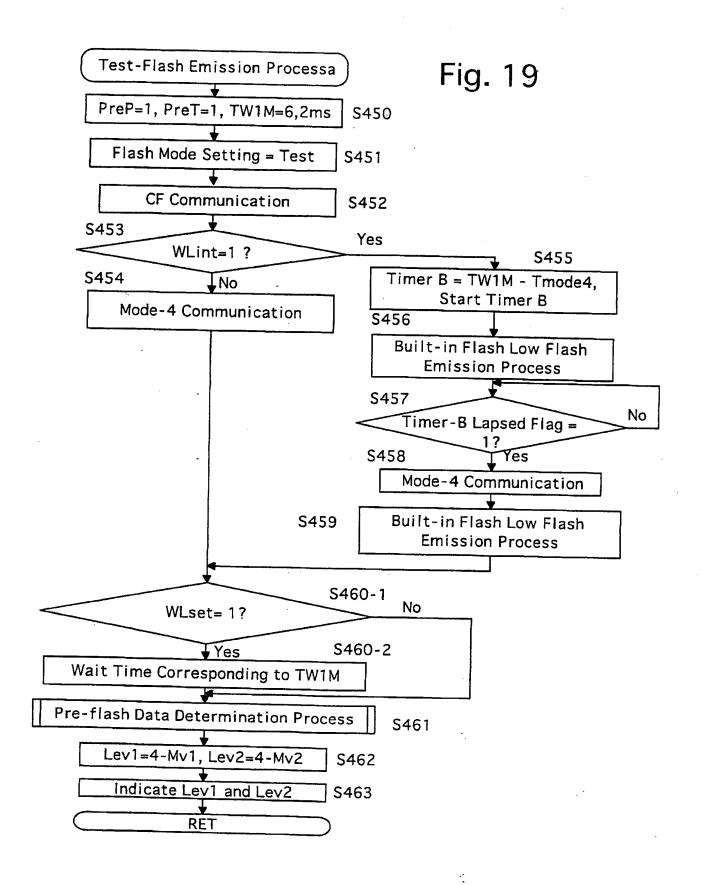


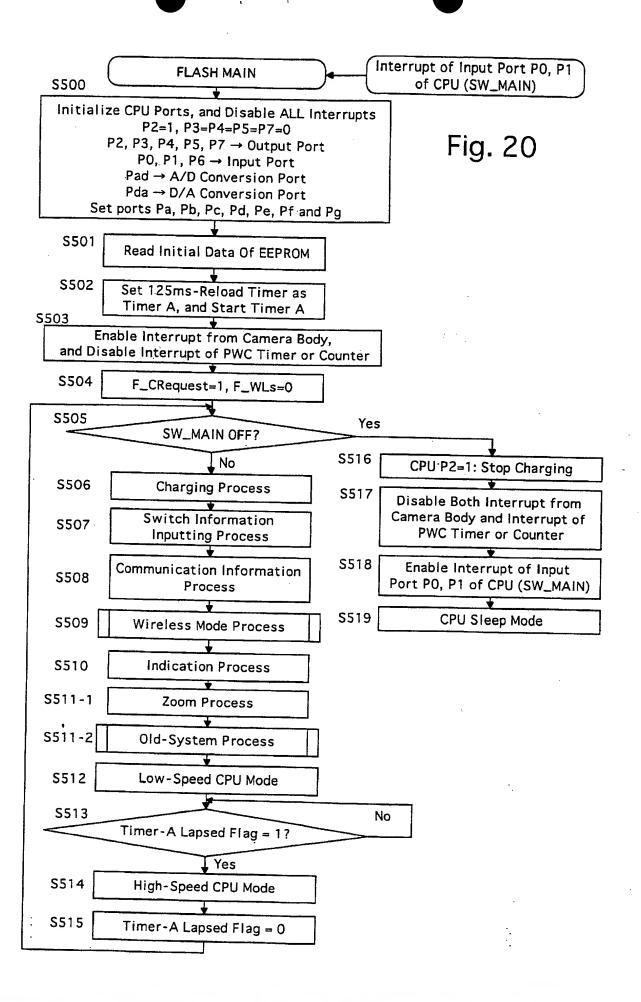


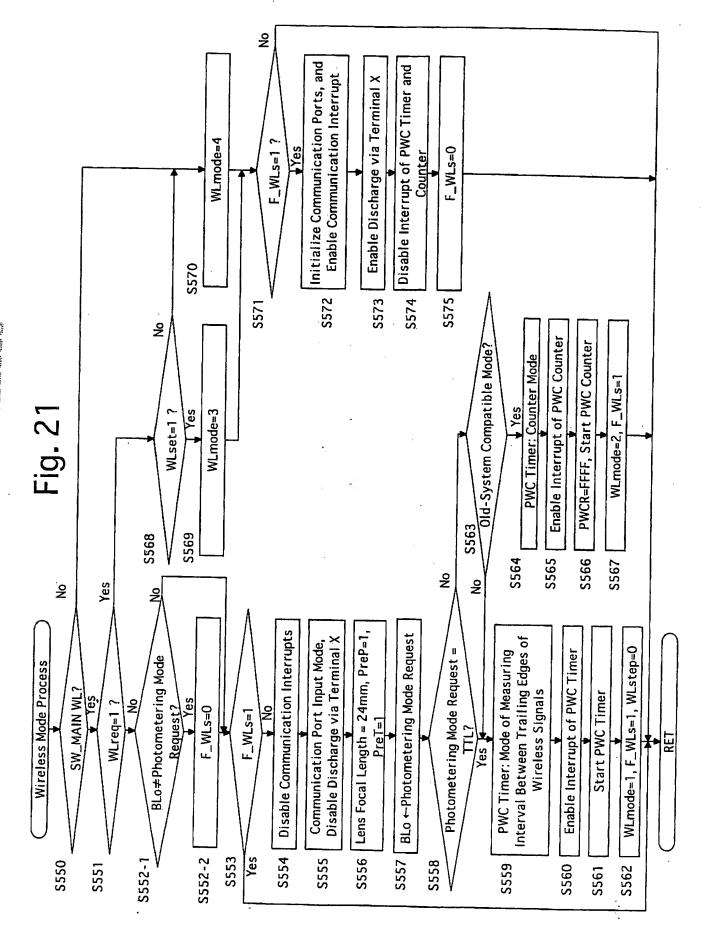


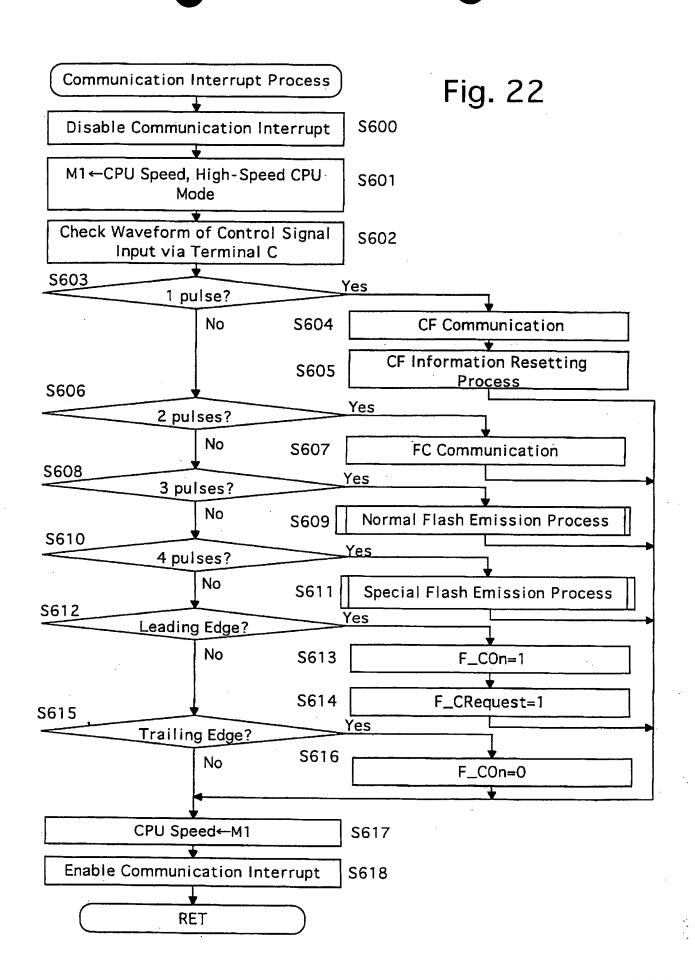


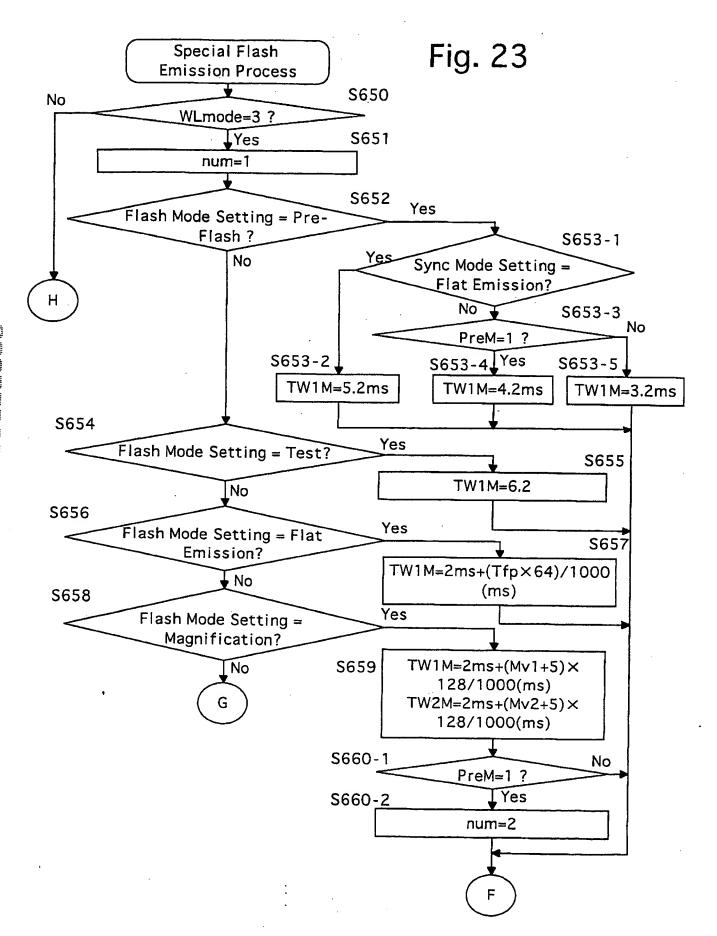


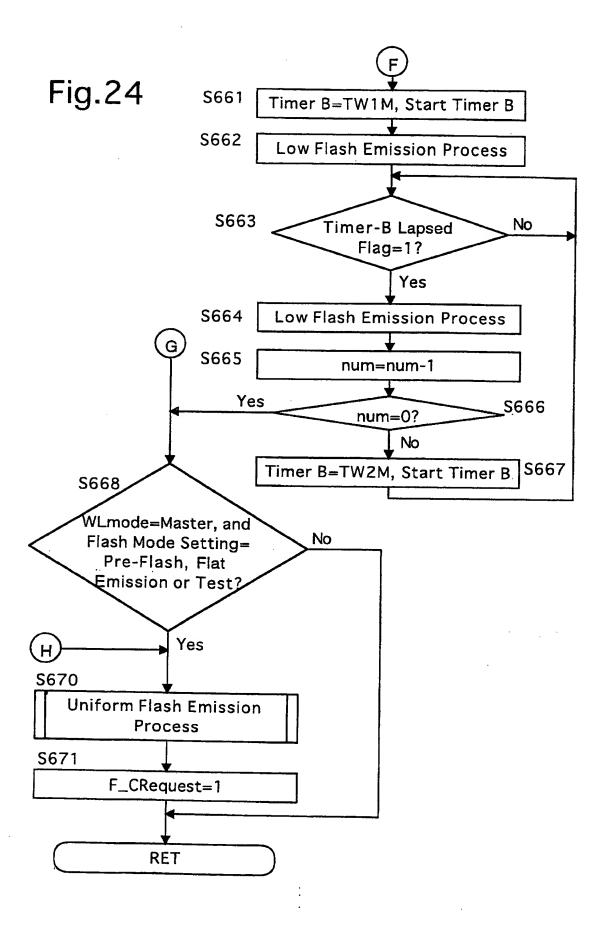


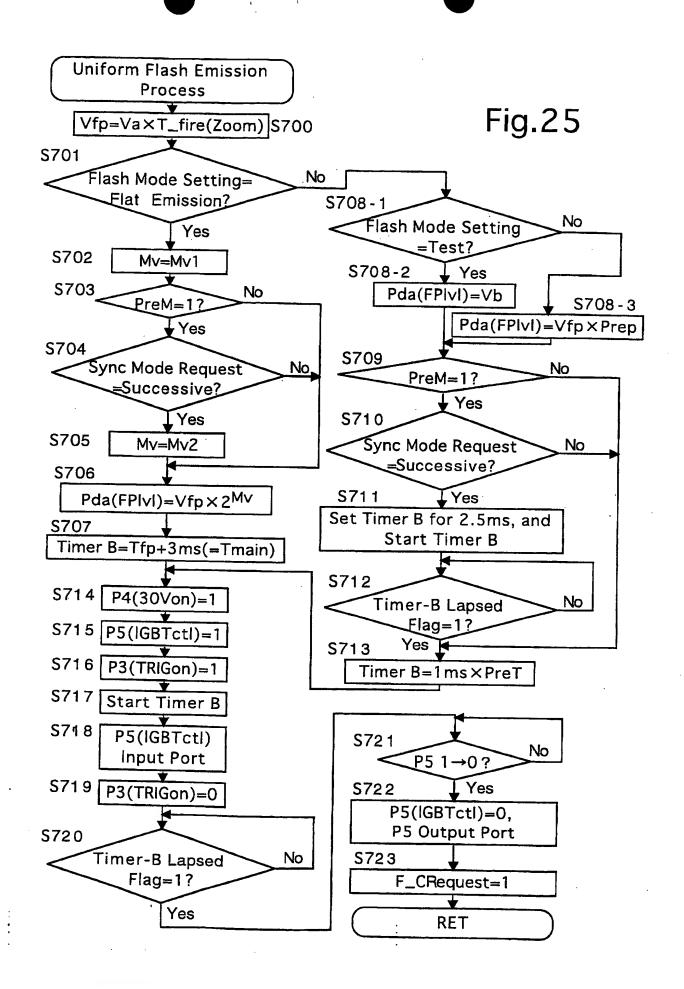


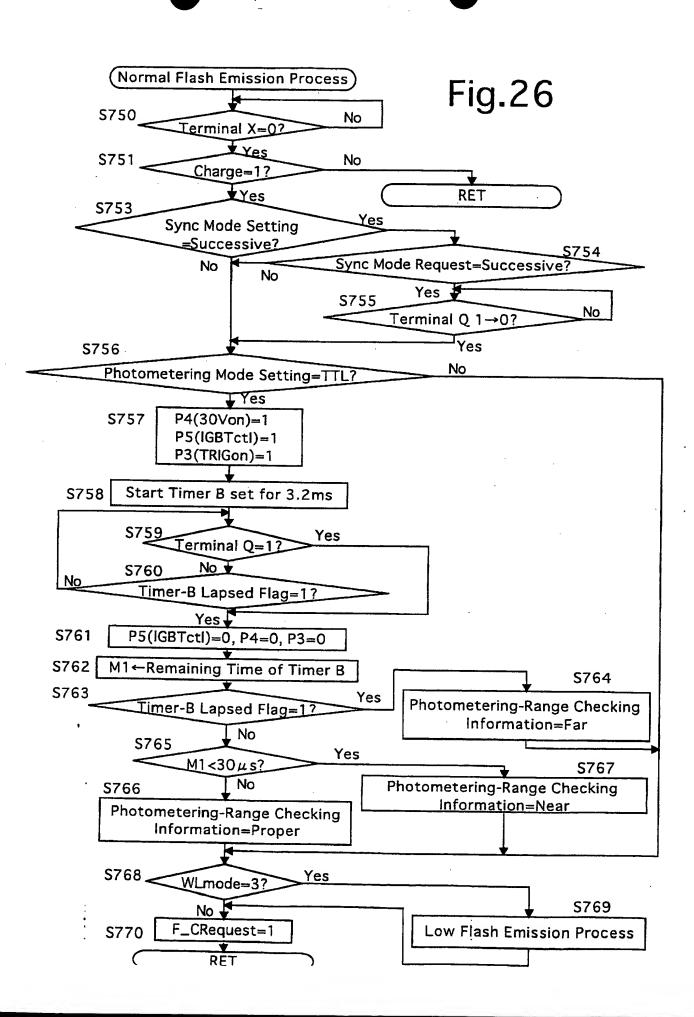


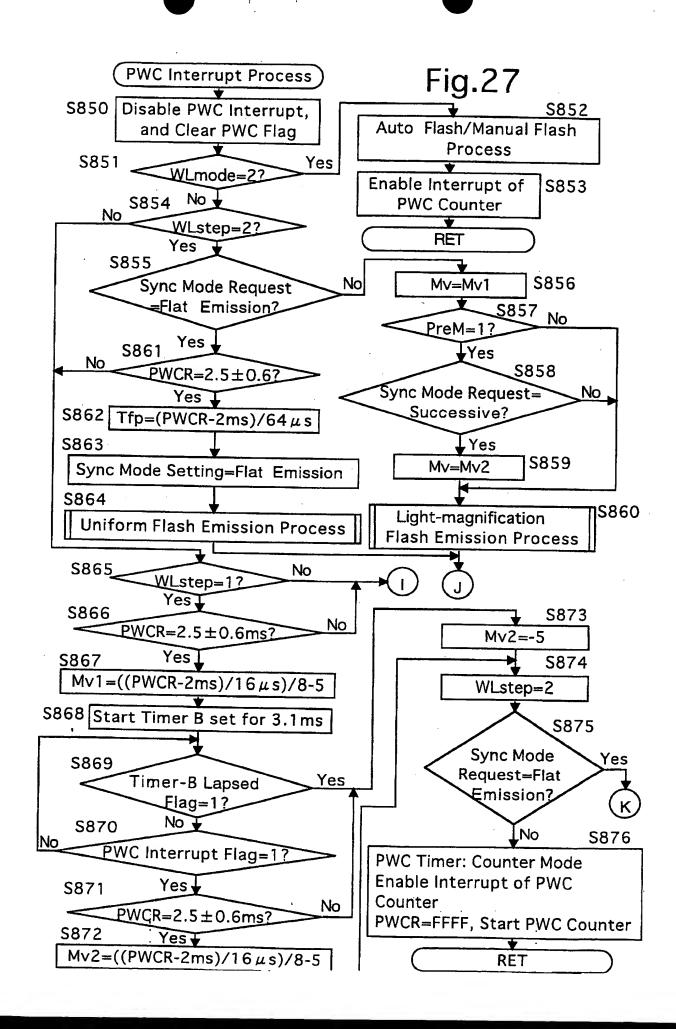


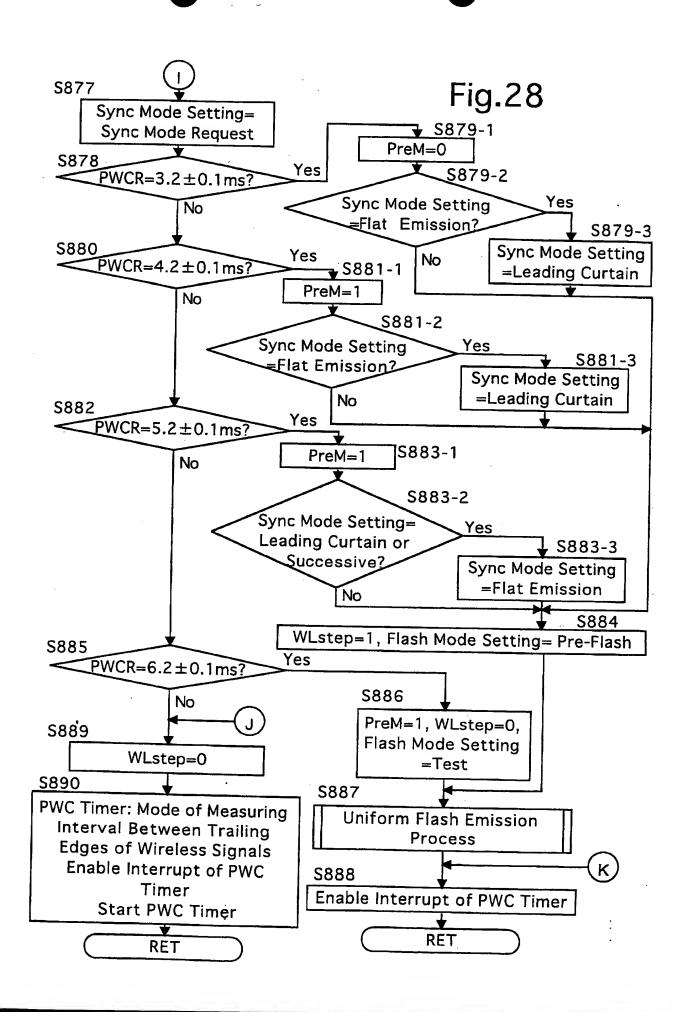












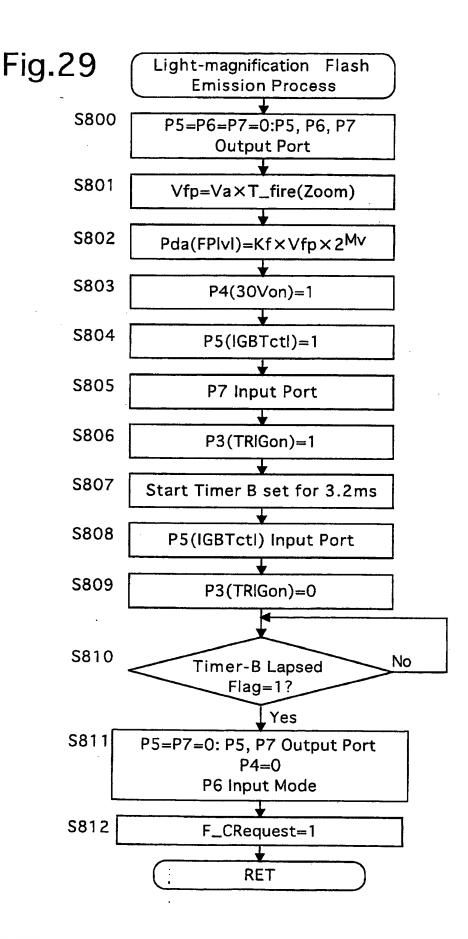


Fig.30

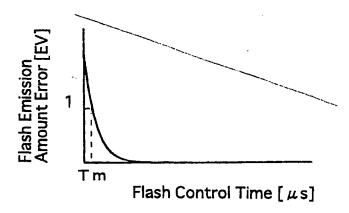


Fig.31

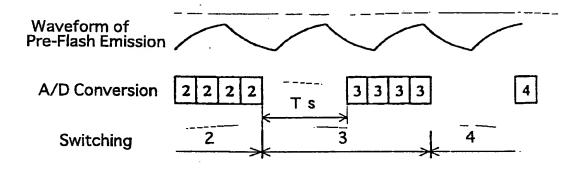


Fig.35

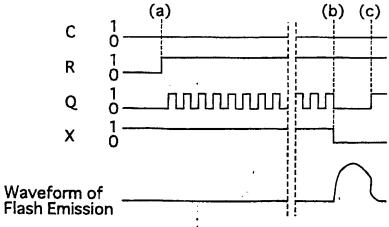


Fig.32

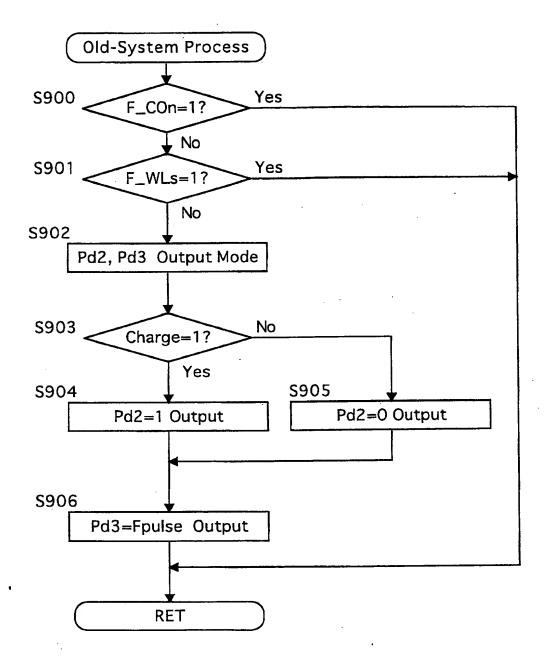


Fig.33

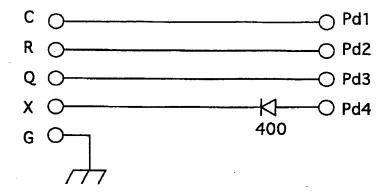


Fig.34

